

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	482	((H1 ROSHI) near2 (YAMADA)).INV.	US-PGPUB; USPAT	OR	ON	2009/04/01 14:42
L2	15	((TOMONORI) near2 (ABE)).INV.	US-PGPUB; USPAT	OR	ON	2009/04/01 14:42
L3	7	((KEIICHI) near2 (TAKI KOSHI)).INV.	US-PGPUB; USPAT	OR	ON	2009/04/01 14:42
L4	19	1 and @pd>="20081124"	US-PGPUB; USPAT	OR	ON	2009/04/01 14:42
L5	0	4 and "printed circuit board"	US-PGPUB; USPAT	OR	ON	2009/04/01 14:43
L6	1	2 and @pd>="20081124"	US-PGPUB; USPAT	OR	ON	2009/04/01 14:43
L7	12587	((361/709,710,717,719) or (257/713,721-722) or (165/80,2,80,3,185) or (174/16.1,16.3)).OQLS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/04/01 14:43
L8	6350	L7 and (PCB "printed circuit board" "circuit board" "substrate")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/01 14:43
L9	199	8 and @pd>="20081124"	US-PGPUB; USPAT	OR	ON	2009/04/01 14:43
L10	173	8 and (leads near2 (aperture openings holes))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/01 14:49
L11	541	8 and controller	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/01 14:58
L12	203	11 and @pd<="20030318"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/01 14:58

L13	11175	controller near2 (housing enclosure chassis)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/01 15:04
L14	4648	13 and @pd<="20030318"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/01 15:04
L15	782	14 and "circuit board"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/01 15:05
L16	799	(361/730).CCLS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/04/01 15:07
L17	588	16 and @pd<="20030318"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/01 15:07

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